

# 2A, 200V - 1000V Standard Surface Mount Rectifier

#### **FEATURES**

- Glass passivated chip junction
- Ideal for automated placement
- Low reverse leakage
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- General purpose

#### **MECHANICAL DATA**

- Case: DO-214AC (SMA)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.060g (approximately)

KEY PARAMETERS			
PARAMETER VALUE UNI			
I <sub>F</sub>	2	Α	
$V_{RRM}$	200 - 1000	V	
I <sub>FSM</sub>	50	Α	
$T_{JMAX}$	150	°C	
Package	DO-214AC (SMA)		
Configuration	Single die		









DO-214AC (SMA)



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)								
PARAMETER		SYMBOL	S2DA-T	S2GA-T	S2JA-T	S2KA-T	S2MA-T	UNIT
Marking code on the device			S2DA	S2GA	S2JA	S2KA	S2MA	V
Repetitive peak reverse voltage	)	$V_{RRM}$	200	400	600	800	1000	V
Reverse voltage, total rms value	Э	V <sub>R(RMS)</sub>	140	280	420	560	700	V
Forward current		I <sub>F</sub>	2					Α
Surge peak forward current, single half sine-wave	t = 8.3ms				50			Α
superimposed on rated load t = 1.0ms		I <sub>FSM</sub>	124					Α
Junction temperature T <sub>J</sub>		-55 to +150				°C		
Storage temperature		T <sub>STG</sub>	-55 to +150		°C			



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THERMAL PERFORMANCE			
PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	R <sub>eJL</sub>	14	°C/W
Junction-to-ambient thermal resistance	R <sub>OJA</sub>	86	°C/W
Junction-to-case thermal resistance	R <sub>eJC</sub>	23	°C/W

**Thermal Performance Note:** Units mounted on PCB (5mm x 5mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C		0.90	-	V
	I <sub>F</sub> = 2A, T <sub>J</sub> = 25°C	V <sub>F</sub>	0.96	1.10	V
	I <sub>F</sub> = 1A, T <sub>J</sub> = 125°C		0.79	-	V
	I <sub>F</sub> = 2A, T <sub>J</sub> = 125°C		0.86	0.97	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>	T <sub>J</sub> = 25°C		-	5	μA
	T <sub>J</sub> = 125°C	- I <sub>R</sub>	-	100	μA
Junction capacitance	$1MHz, V_R = 4.0V$	CJ	30	-	pF

#### Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING	
S2xA-T	DO-214AC (SMA)	7,500 / Tape & Reel	

#### Notes:

1. "x" defines voltage from 200V(S2DA-T) to 1000V(S2MA-T)



#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

**Fig.1 Forward Current Derating Curve** 

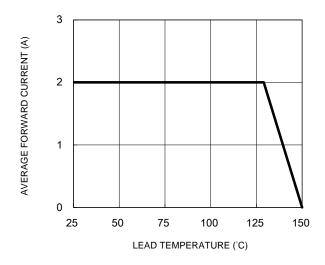


Fig.3 Typical Reverse Characteristics

PERCENT OF RATED PEAK REVERSE VOLTAGE (%)

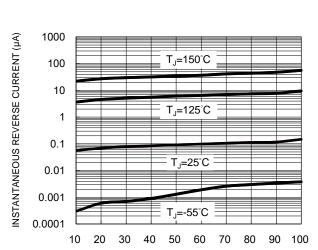


Fig.2 Typical Junction Capacitance

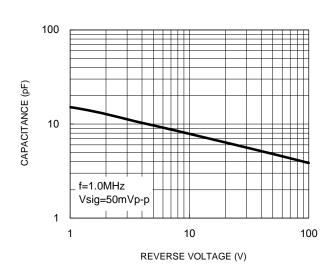


Fig.4 Typical Forward Characteristics

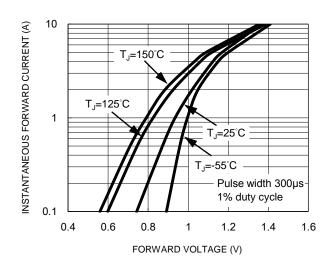
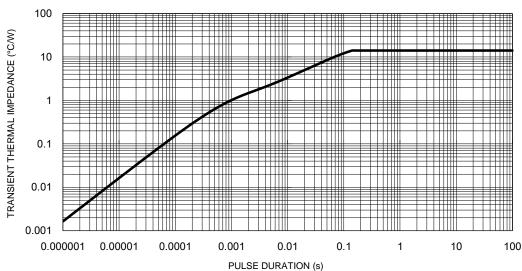


Fig.5 Typical Transient Thermal Impedance

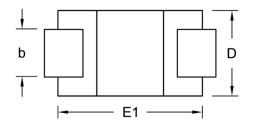


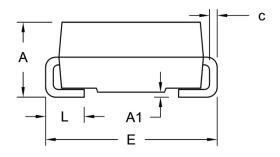




## **PACKAGE OUTLINE DIMENSIONS**

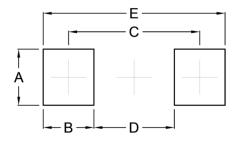
## DO-214AC (SMA)





DIM.	Unit (mm)		Unit (	(inch)
Dilvi.	Min.	Max.	Min.	Max.
Α	1.99	2.50	0.078	0.098
A1	0.05	0.20	0.002	0.008
b	1.27	1.58	0.050	0.062
С	0.15	0.31	0.006	0.012
D	2.29	2.83	0.090	0.111
E	4.95	5.33	0.195	0.210
E1	4.06	4.60	0.160	0.181
L	0.90	1.41	0.035	0.056

### **SUGGESTED PAD LAYOUT**



Symbol	Unit (mm)	Unit (inch)
Α	1.68	0.066
В	1.52	0.060
С	3.93	0.155
D	2.41	0.095
E	5.45	0.215

## **MARKING DIAGRAM**



= Marking Code P/N G = Green Compound

= Date Code ΥW F = Factory Code



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